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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/737,324	12/14/2000	Mikko Antero Lipsanen	032986-011	9513
27045	7590	06/01/2005	EXAMINER	
ERICSSON INC. 6300 LEGACY DRIVE M/S EVR C11 PLANO, TX 75024			SEFCHECK, GREGORY B	
			ART UNIT	PAPER NUMBER
			2662	

DATE MAILED: 06/01/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	09/737,324	LIPSANEN, MIKKO ANTERO	
	Examiner	Art Unit	
	Gregory B. Sefcheck	2662	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 31 January 2005.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 20-35 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 20-35 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. _____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|--|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____ | 6) <input type="checkbox"/> Other: _____ |

DETAILED ACTION

- Applicant's Amendment filed 1/31/2005 is acknowledged.
- Claims 20 and 32-35 have been amended.
- Claims 1-19 and 36-38 have been cancelled.
- Claims 20-35 remain pending.

Claim Rejections - 35 USC § 103

1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

2. Claims 20-23, 25, 26, and 28-35 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kainulainen (US005796793A) in view of Dolev et al. (US005784421A), hereafter Dolev.

- In regards to Claims 20, 21, and 32-35,

Kainulainen discloses synchronizing the slave nodes in a telecommunications system to a single master clock source utilizing a hierarchical message-based synchronization (Abstract; claim 20,32,33,34,35 – node synchronizing in a network, where a master node is coupled to a PRC and a plurality of slave nodes synchronize their internal clocks to the PRC using data received on incoming links).

Referring to Fig. 3, Kainulainen shows propagating synchronization messages through the network from the master node 1 to slave nodes 2-5. Kainulainen shows that the propagation of the message is delayed so the slave nodes may adapt the message signature to incorporate its identify into the message (Col. 3-4, lines 55-59; claim 20,32,33,34,35 – propagating sync status messages through network from the master node through slave nodes, with each slave node incorporating its identify into the message as it propagates to generate a message path and propagating the message on to neighboring slave nodes).

In an ideally stable situation, Kainulainen discloses all nodes would be synchronized to the master node. Kainulainen shows that certain nodes may receive two or more incoming signals synchronized with the master code, so additional processing delay is required to select the signal based upon the one arriving over the shortest path (Col. 4, lines 37-43; claim 20,32,33,34 – introducing an additional delay in the propagation at certain nodes).

Kainulainen also shows incrementing a distance counter in the message when received by a slave node (Col. 4, lines 14 and 49; claim 34 – slave node increments a distance counter in the SSM).

Kainulainen shows that messages may be received on multiple incoming links, with the link having the shortest path messages being selected for synchronization (Col. 4, lines 21-40; claim 20,32,34,35 – registering a path length of a SSM received on an incoming link as an attribute for that link; claim 21 –

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selecting the incoming link having an attribute indicating the shortest path length from the master node as the link to synchronize on).

Kainulainen does not explicitly show introducing an additional delay in the propagation of synchronization messages in at least certain of the network nodes.

Dolev discloses a network node performing anonymous time synchronization in a network. Referring to Fig. 6, Dolev shows that a node, upon receiving a sync message from another node in the network, a random delay period is introduced before any subsequent processing is performed (Col. 20, lines 7-40; claim 20,32,33,34,35 – waiting predetermined time to introduce additional delay in propagation of sync message).

It would have been obvious to one of ordinary skill in the art at the time of the invention to modify the method and system of Kainulainen by implementing an additional delay in the propagation of sync messages, as shown by Dolev. This delay would serve to avoid collisions on the network, reducing the likelihood that multiple nodes having received the sync message will all try to send a subsequent message at the same time.

- In regards to Claim 22,

Kainulainen discloses synchronizing the slave nodes in a telecommunications system to a single master clock source utilizing a

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hierarchical message-based synchronization that covers all limitations of the parent claim.

Referring to Figs. 2 and 3, Kainulainen shows that the message-based synchronization (Fig. 3) begins upon initialization of the network (Fig. 2; Col. 5, lines 35-57; claim 22 – propagating SSMS on initializing a new network).

- In regards to Claim 23,

Kainulainen discloses synchronizing the slave nodes in a telecommunications system to a single master clock source utilizing a hierarchical message-based synchronization that covers all limitations of the parent claim.

Kainulainen shows that the nodes transmit sync messages in all directions to accommodate changes in the network (Col. 4-5, lines 60-25; claim 23 – sending SSMS at intervals to enable the network to cope with dynamic changes in network architecture).

- In regards to Claim 25,

Kainulainen discloses synchronizing the slave nodes in a telecommunications system to a single master clock source utilizing a hierarchical message-based synchronization that covers all limitations of the parent claim.

Kainulainen shows that a node, upon accepting a message from one of its neighboring nodes, generates its own outgoing message with its unique

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signature incorporated into the message (Col. 4, lines 44-47; claim 25 – generating a SSM at a slave node in response to receipt of a SS request message from a neighbor slave node, with the SSM including identification of the path over which the sending slave node has been synchronized).

- In regards to Claim 26,

Kainulainen discloses synchronizing the slave nodes in a telecommunications system to a single master clock source utilizing a hierarchical message-based synchronization that covers all limitations of the parent claim.

Kainulainen shows that a node transmits a sync message after incrementing a counter indicative of its distance from the master node (Col. 4, lines 14 and 49; claim 26 – a node which SSM passes adds its distance from the master node).

A node utilizes the distance information in the message of each incoming link to select the link having the shortest path distance from the master node (Col. 4, lines 21-39; claim 26 – a node registers the distance as an attribute for each incoming link).

- In regards to Claims 28-31,

Kainulainen discloses synchronizing the slave nodes in a telecommunications system to a single master clock source utilizing a

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hierarchical message-based synchronization that covers all limitations of the parent claim.

Kainulainen shows that the delay for propagation and selection of the sync messages is a preset time period (Col. 7, lines 60-63; claim 28 – additional delay introduced by a slave node is the same for all slave nodes that introduce delay).

Kainulainen also shows that the propagation of the sync messages may be delayed based on the distance and signature information contained within the received sync messages that are stored in a table in the node's memory (Col. 7-9, lines 60-55; claim 29 – additional delay increases with distance from the master node; claim 30 – additional delay to be introduced by a node is identified in the SSM; claim 31 – additional delay to be introduced by a node is defined by a delay table stored in the node).

3. Claims 24 and 27 rejected under 35 U.S.C. 103(a) as being unpatentable over Kainulainen in view of Dolev further in view of Tikalsky (US005875179A).

- In regards to Claim 24,

Kainulainen discloses synchronizing the slave nodes in a telecommunications system to a single master clock source utilizing a hierarchical message-based synchronization that covers all limitations of the parent claim.

Kainulainen does not explicitly show generating SSMs at the master node in response to a synchronization request from another node.

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Tikalsky discloses a method for synchronizing nodes through sync messages derived from a single reference clock of a master node in a wireless network. Tikalsky shows that sync messages may be initiated by a request from another node (Col. 4, lines 57-65; claim 24 – SSMs are generated in response to receipt at the master node of a SS request message from another node).

It would have been obvious to one of ordinary skill in the art at the time of the invention to adapt the node synchronization of Kainulainen by generating sync messages in response to a request from another node. This modification would allow synchronization of new nodes that were not able to synchronize in a previous sync message exchange.

- In regards to Claim 27,

Kainulainen discloses synchronizing the slave nodes in a telecommunications system to a single master clock source utilizing a hierarchical message-based synchronization that covers all limitations of the parent claim.

Kainulainen does not explicitly show synchronization of nodes in a UMTS network.

Tikalsky discloses a method for synchronizing nodes through sync messages derived from a single reference clock of a master node in a wireless network (Title; claim 27 – network is a UMTS network).

It would have been obvious to one of ordinary skill in the art at the time of the invention to apply the node synchronization of Kainulainen to a wireless

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network, such as a UMTS network, as taught by Tikalsky, thereby enabling node synchronization in the dynamically changing and reconfigurable environment of a wireless network.

Response to Arguments

4. Applicant's arguments with respect to claims 20-35 have been considered but are moot in view of the new ground(s) of rejection.

Conclusion

5. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure.

- Holloway et al. (US006747996B2) discloses synchronized transport across non-synchronous networks
- Haartsen (US006028853A) discloses a method and arrangement for radio communications
- Mustonen (US005124698A) discloses a method and apparatus for synchronizing radio transmitters in a paging network
- Franaszek et al. (US005001730) discloses a clock synchronization algorithm for address independent networks

6. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL.**

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See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Gregory B. Sefcheck whose telephone number is 571-272-3098. The examiner can normally be reached on Monday-Friday, 8:00am-4:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Hassan Kizou can be reached on 571-272-3088. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

GBS
5-19-2005



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